

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
1	BRS	L1	38435	(driver or transmit\$4) near10 delay\$3	USPA T; US-P GPUB	2004/08/31 19:12	
2	BRS	L2	4139	programmabl\$1 near3 delay\$2	USPA T; US-P GPUB	2004/08/31 19:13	
3	BRS	L3	1041	1 and 2	USPA T; US-P GPUB	2004/08/31 19:13	
4	BRS	L4	69451	(trace or path) near3 length	USPA T; US-P GPUB	2004/08/31 19:14	
5	BRS	L5	168	3 and 4	USPA T; US-P GPUB	2004/08/31 19:15	
6	BRS	L6	56	2 same 4	USPA T; US-P GPUB	2004/08/31 19:15	
7	BRS	L7	12	5 and 6	USPA T; US-P GPUB	2004/08/31 19:33	
8	BRS	L8	36409	latch\$3 near3 data	USPA T; US-P GPUB	2004/08/31 19:34	
9	BRS	L9	30220	phase near3 clock	USPA T; US-P GPUB	2004/08/31 19:35	
10	BRS	L10	4	6 and 8	USPA T; US-P GPUB	2004/08/31 19:45	
11	BRS	L11	73	5 and 9	USPA T; US-P GPUB	2004/08/31 19:45	
12	BRS	L12	24	8 and 11	USPA T; US-P GPUB	2004/08/31 19:45	
13	BRS	L13	21	12 not 10	USPA T; US-P GPUB	2004/08/31 19:45	

	Type	L #	Hits	Search Text	DBs	Time Stamp	Comments
14	BRS	L14	21	13 not 7	USPA T; US-P GPUB	2004/08/3 1 20:13	
15	BRS	L15	1409	713/400.ccls. 713/401.ccls. 713/503.ccls.	USPA T; US-P GPUB	2004/08/3 1 20:13	
16	BRS	L16	1	6 and 15	USPA T; US-P GPUB	2004/08/3 1 20:14	
17	BRS	L17	10	5 and 15	USPA T; US-P GPUB	2004/08/3 1 20:14	